"SELF-ALIGNED PROCESS USING INDIUM GALLIUM ARSENIDE ETCHING TO FORM REENTRY FEATURE IN HETEROJUNCTION BIPOLAR TRANSISTOR" Scott A. McHugo et al.

10030222-1 1/9

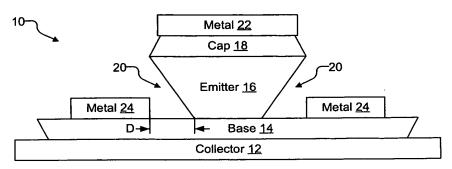


Fig. 1 (Prior Art)

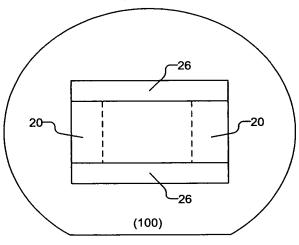


Fig. 2A (Prior Art)

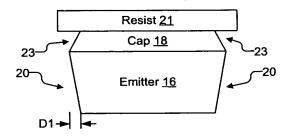


Fig. 2B (Prior Art)

"SELF-ALIGNED PROCESS USING INDIUM GALLIUM ARSENIDE ETCHING TO FORM REENTRY FEATURE IN HETEROJUNCTION BIPOLAR TRANSISTOR" Scott A. Machingo et al.

10030222-1 2/9

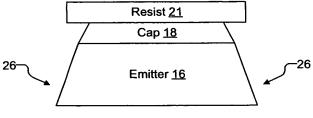


Fig. 2C (Prior Art)

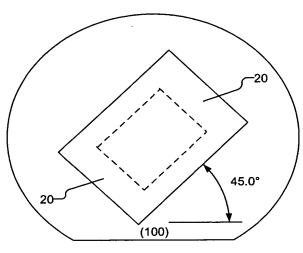


Fig. 3A (Prior Art)

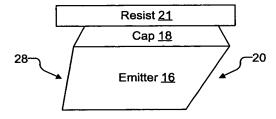


Fig. 3B (Prior Art)

"SELF-ALIGNED PROCESS USING INDIUM GALLIUM ARSENIDE ETCHING TO FORM REENTRY FEATURE IN HETEROJUNCTION BIPOLAR TRANSISTOR" Scott A. McHugo et al.

10030222-1 3/9

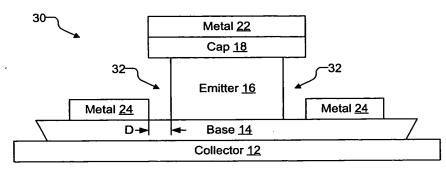


Fig. 4 (Prior Art)

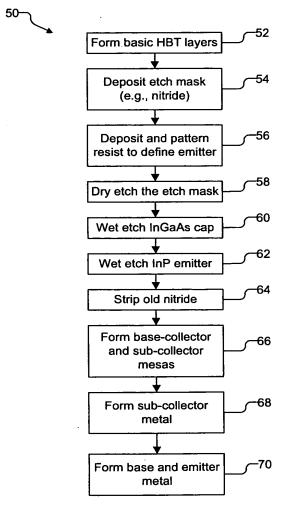


Fig. 5

"SELF-ALIGNED PROCESS USING INDIUM GALLIUM ARSENIDE ETCHING TO FORM REENTRY FEATURE IN HETEROJUNCTION BIPOLAR TRANSISTOR" Scott A. McHugo et al. 10030222-1

4/9

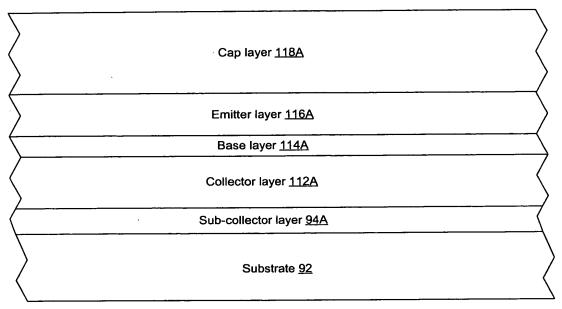


Fig. 6A

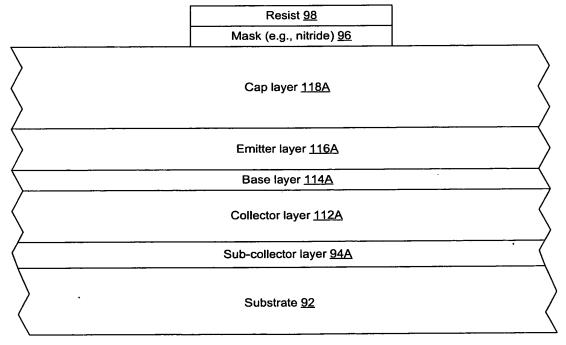


Fig. 6B

"SELF-ALIGNED PROCESS USING INDIUM GALLIUM ARSENIDE ETCHING TO FORM REENTRY FEATURE IN HETEROJUNCTION BIPOLAR TRANSISTOR" Scott A. McHugo et al. 10030222-1

5/9

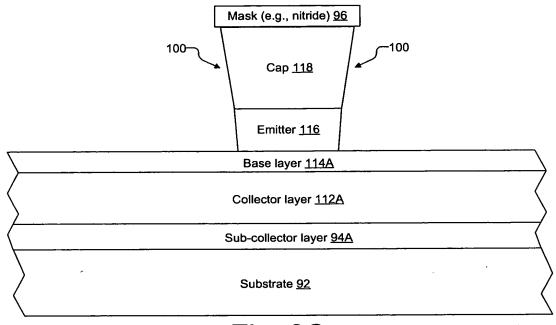


Fig. 6C

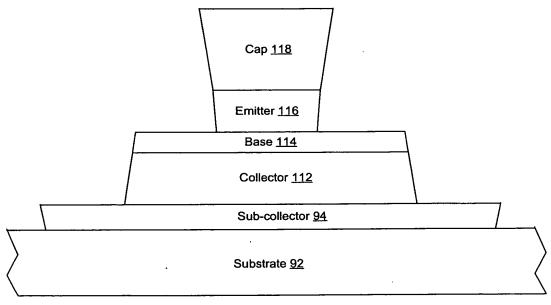
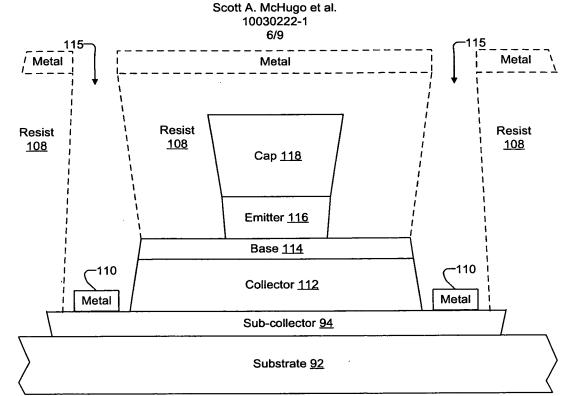
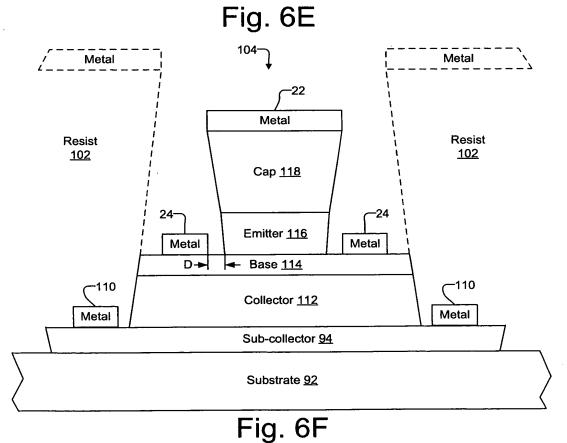


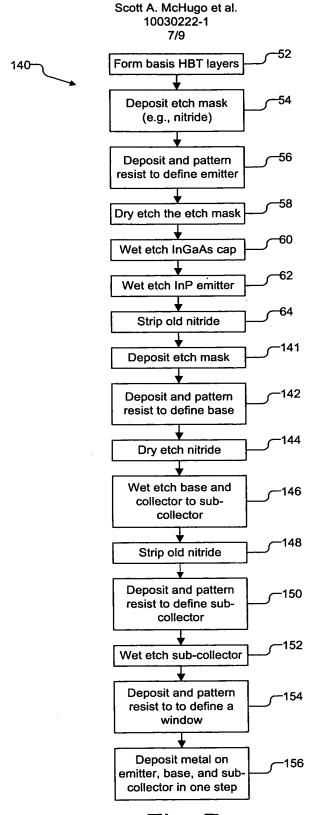
Fig. 6D

"SELF-ALIGNED PROCESS USING INDIUM GALLIUM ARSENIDE ETCHING TO FORM REENTRY FEATURE IN HETEROJUNCTION BIPOLAR TRANSISTOR"





"SELF-ALIGNED PROCESS USING INDIUM GALLIUM ARSENIDE ETCHING TO FORM REENTRY FEATURE IN HETEROJUNCTION BIPOLAR TRANSISTOR"



"SELF-ALIGNED PROCESS USING INDIUM GALLIUM ARSENIDE ETCHING TO FORM REENTRY FEATURE IN HETEROJUNCTION BIPOLAR TRANSISTOR" Scott A. McHugo et al. 10030222-1 8/9

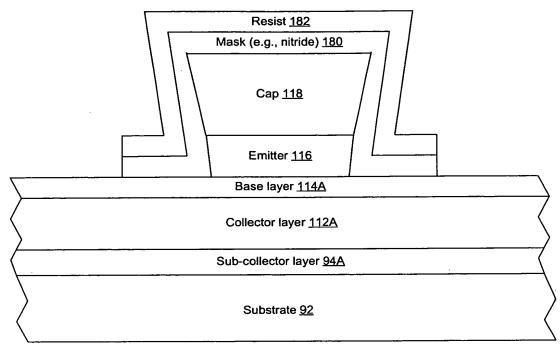


Fig. 8A

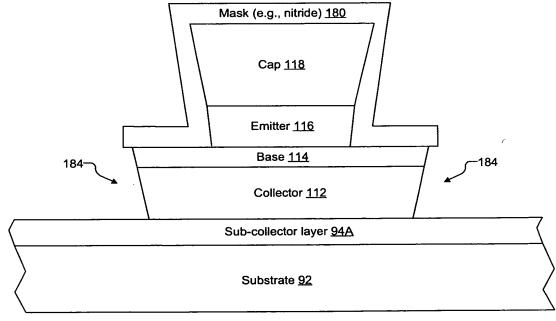


Fig. 8B

"SELF-ALIGNED PROCESS USING INDIUM GALLIUM ARSENIDE ETCHING TO FORM REENTRY FEATURE IN HETEROJUNCTION BIPOLAR TRANSISTOR" Scott A. McHugo et al.

10030222-1 9/9

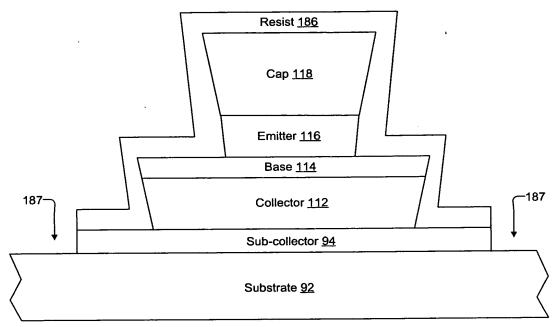


Fig. 8C

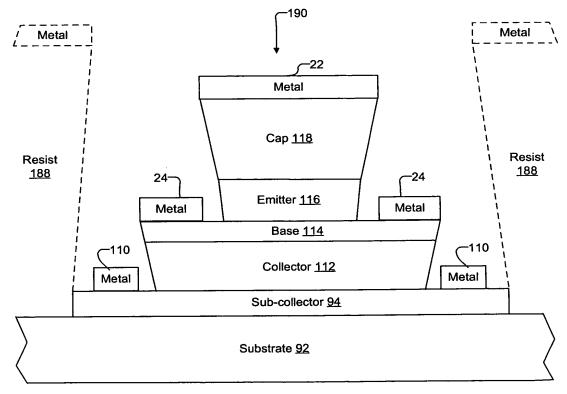


Fig. 8D